## Brief biodata

C.E.Veni Madhavan obtained his academic degrees, B.E.(Electrical Engineering) from College of Engineering, Madras, in 1969, M.E (Control Systems) from BITS Pilani, in 1971 and PhD (Control theory) from Indian Institute of Science in 1975. He worked in the industry, Air India (1976-1977), National Informatics Center (1977-1982), before joining the Computer Science and Automation department of IISc in 1983. He was a senior professor in the CSA department and retired formally in August 2014. He was the Chairman of CSA department during 1998-2000. He was invited to head the DRDO laboratory, SAG, New Delhi, during 2000-2003.

His current research areas of interest are computational linguistics and pragmatic cryptanalysis. He continues to work, in IISc, on three R&D projects in the areas of cryptography and natural language processing and on several technical advisory tasks spanning government policy and academic and research mentorship.

He has worked on many scientific, industrial consulting and development projects sponsored by government and corporate sectors in these areas. He has published over 80 papers in refereed journals and conferences, and delivered over 200 invited talks in national conferences and universities. He has guided 16 PhD, 12 MS and over 100 ME theses. He is the co-author of a book on Public-key Cryptography published in India in 2009. He is the co-author of a recently filed Indian Patent on a New encryption algorithm. An international patent on this is being filed.

Some of his project engagements were in the areas of cryptanalysis (MCIT), cryptography (TCoE-DOT) and cognitive networks (DST, DRDO). He was a co-investigator in a multi-institutional consortium project funded by CEFIPRA-IFCPAR (2012-2015). The collaborative project between, IISc (his group), TCS, University of Nice (France) and Gemalto (France), was on the design and development of various financial inclusion platforms on the mobile telephone infrastructure. He developed a system for handling ecash and ewallet applications based on a novel design of a digital analogue of denominational fiat currency. He also built a cash-transaction simulator. He was the General Chair of the conference ADCOM 2017 of ACSSS on the theme of block chains and cryptocurrencies and the Chairman of the INAE Engineers Conclave 2017 on the theme digital economy.

He obtained in 2001, an award by the Mathematical Association of India, for distinguished services in mathematics education and research. He obtained in March 2011, an award by IISc, for Excellence in Research in Engineering. He and co-authors obtained a Best Paper Prize in July 2011, in Boston USA, in the Annual Conference of the Cognitive Science Society.

He was an Associate Faculty of the Centre for Neuroscience of IISc. He was the Chief Executive of the Society for Innovation and Development (SID) of IISc, during 2006-2012. He is closely associated with activities of the professional bodies, the Cryptology Research Society of India (CRSI), the Ramanujan Mathematical Society (RMS), ACS, INAE. He also works very closely with the government policy and R&D funding panels of DST, IFCPAR, MCIT, DRDO, NRB, NSCS, NTRO and with academic councils of universities and research centers, CRRAIMSCS, DSU, SASTRA, AMRITA, as member or Chairman of technical expert committees .